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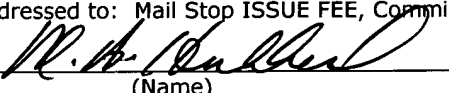
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Kelvin S. Vartti, et. al.
Application No.: 10/600,205 File No: 1.020547000
Filing Date: 06/20/2003 Examiner: Peugh, Brian R.
Allowed: 07/28/2005 Customer: 27516
Title: DATA ACCELERATION MECHANISM FOR A MULTIPROCESSOR SHARED
MEMORY SYSTEM

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By


(Name)

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P O Box 1450
Alexandria, VA 22313-1450

Sir:

The Examiner's statement of Reasons for Allowance implies that the claimed invention was allowed because the prior art did not disclosed specific elements. The elements characterized by the Examiner, however, even if found in the prior art, would not render the claimed invention invalid under 35 USC §102 because the claimed invention includes a number of limitations not addressed in the Reasons for Allowance. With respect to 35 USC §103, the requirements of establishing a *prima facie* case of obviousness including (1) a showing that the prior art teaches the entire claimed invention where all limitations are to be considered, and (2) that combining various prior art references is (a) suggested in the art, and (b) there would be motivation to make the combination, with a likelihood of success, have not been satisfied.

Unless otherwise advised, these comments are intended to be clarifying in a manner consistent with the law.

Respectfully submitted,



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August 25, 2005
Date